

FIT-IT Embedded Systems

FIT-IT Systems on Chip

Austrian Research Funding Programmes

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Austrian Research Promotion Agency (FFG)

FIT-IT '08 – Spring Research
May 8th, 2008, Vienna

A cooperation of



- About FIT-IT
- FIT-IT Embedded Systems
 - Last year's call
 - 8th call focus
- FIT-IT Systems on Chip
 - Last year's call
 - 4th call focus
- General aspects for submission

FIT-IT [

Development of radically new IT
(research prototype)

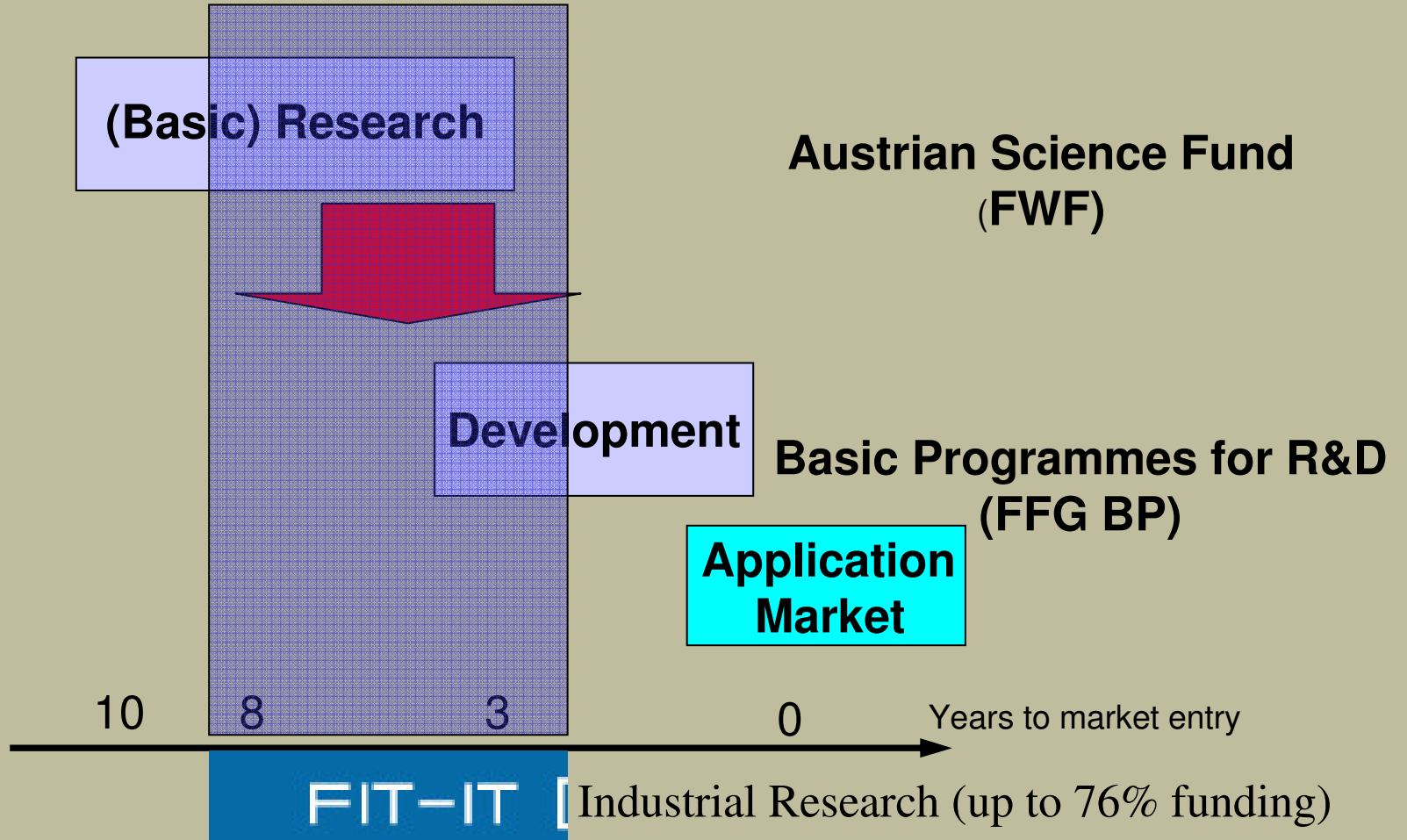
Strengthen the competitiveness of
Austrian research and business
organizations

Train qualified researchers

Improve international visibility

“FIT-IT aims at funding projects with a **high degree of innovation** that **span from basic research** to the first **research prototype**”

(FIT-IT Embedded Systems Guidelines for Proposers, p.8)



- **High quality:**
Top themes, first-class methods, new approaches - risky projects
- Cooperation between companies and research institutes
- Time to market: 3-8 years
- International jury
- No product development

- 7 calls in FIT-IT Embedded Systems (2002-2007) so far
- 16.9 Mio EUR funding granted
- Last year's call:
 - 5 projects funded (out of 11 proposals)
 - total project cost 4.2 Mio EUR
 - funding 2.5 Mio EUR

- iTire – Intelligent tire
- POWER-HOUSE - Power-aware, hardware-supported operating system and ubiquitous application software development environment
- e-HOME - Context-aware and distributed embedded system for assistive home technology
- ECO-SENSOR - Energy harvesting for mobile eco-efficient wireless sensor networks
- FRONTICS - Communication framework for networked automation and control systems

- Model-based design processes of integrated systems
 - Modeling of complex real-world systems
 - Automatic test generation
 - Variant and complexity management
 - Certification aspects
- Adaptive and integrated systems
 - Self-organizing integrated systems
 - Autonomous systems
- In addition to this focus the call is open for all projects in the field of Embedded Systems.

- 3 calls in FIT-IT Systems on Chip (2005-2007) so far
- 10.3 Mio EUR funding granted
- Last year's call:
 - 4 projects funded (out of 5 proposals)
 - total project cost 6.6 Mio EUR
 - funding 3.6 Mio EUR

- SNOPS – Sensor Network Optimizations by Power Simulation
- ARDES – ADC for Deep Submicron Technologies
- PUMA – Power Management Solutions for Next Generation Mobile Applications
- M²RX – Mobile Multicast Receiver

To hold and expand Austria's high position in the area of „Systems on Chip“ with:

- applied research and concentration on radical innovation in Systems on Chip
- industrially realizable technologies in Austria

- Focus on design processes of complex circuits:
 - Mixed-signal design
 - Digitally assisted analog circuits
- Special Topic: Research on supporting processes within System on Chip Design
 - HW/SW-Codesign
 - Reliability in Package
 - Process support
 - Sensor technology for the connection of actual and prospective SoCs and SiPs.
- In addition to this focus, the call is open for the whole area of „Systems on Chip“

Proposers are encouraged to:

- submit projects with clear impact
- set up larger project consortia (e.g., 4-6 partners)
- cooperate with small and medium size enterprises
- (read the “Leitfaden für Einreicher”)

Call Deadline: June 30th, 2008 12:00

Take up the FIT-IT challenge!

Further information and pre-proposal checks:

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